

Figure 1





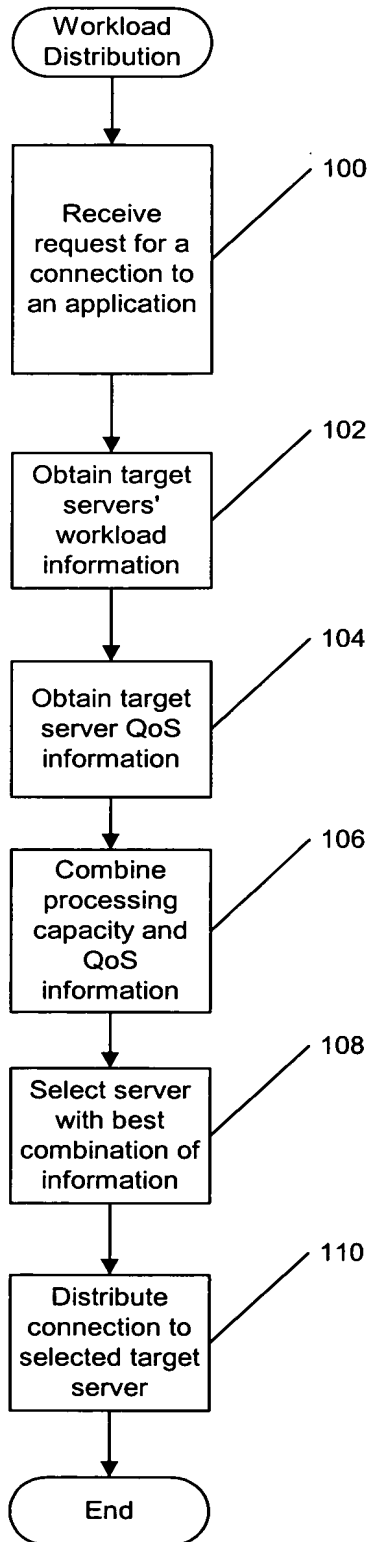


Figure 4

FIG. 5 is a block diagram of a system architecture.

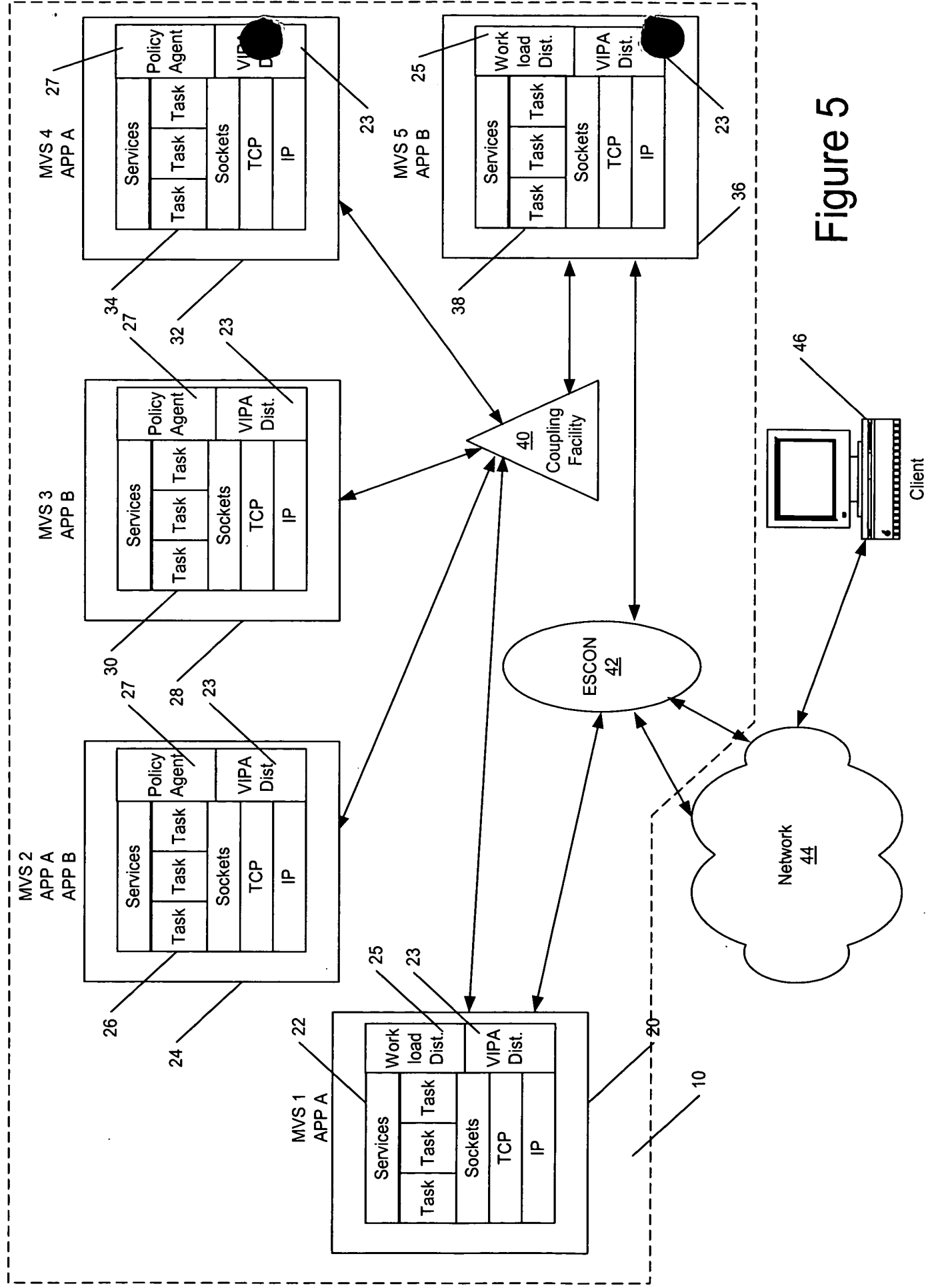


Figure 5

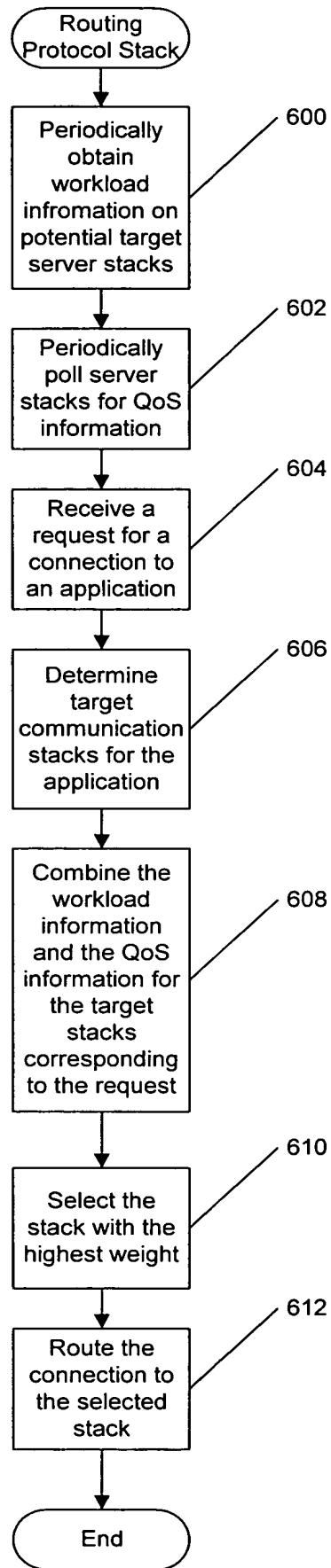


Figure 6

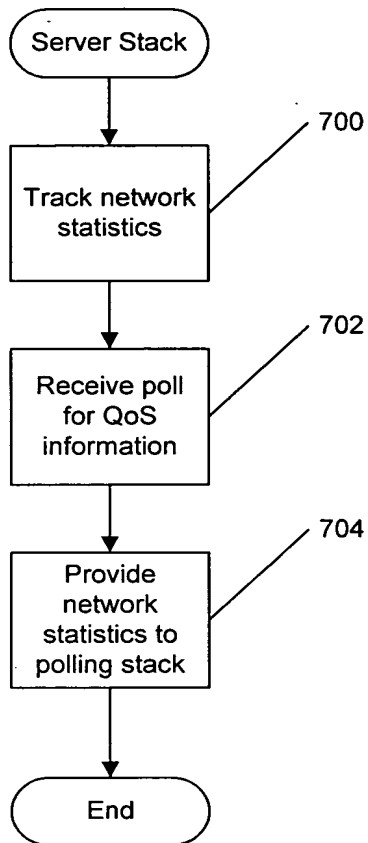


Figure 7